

Figure 1: T1R

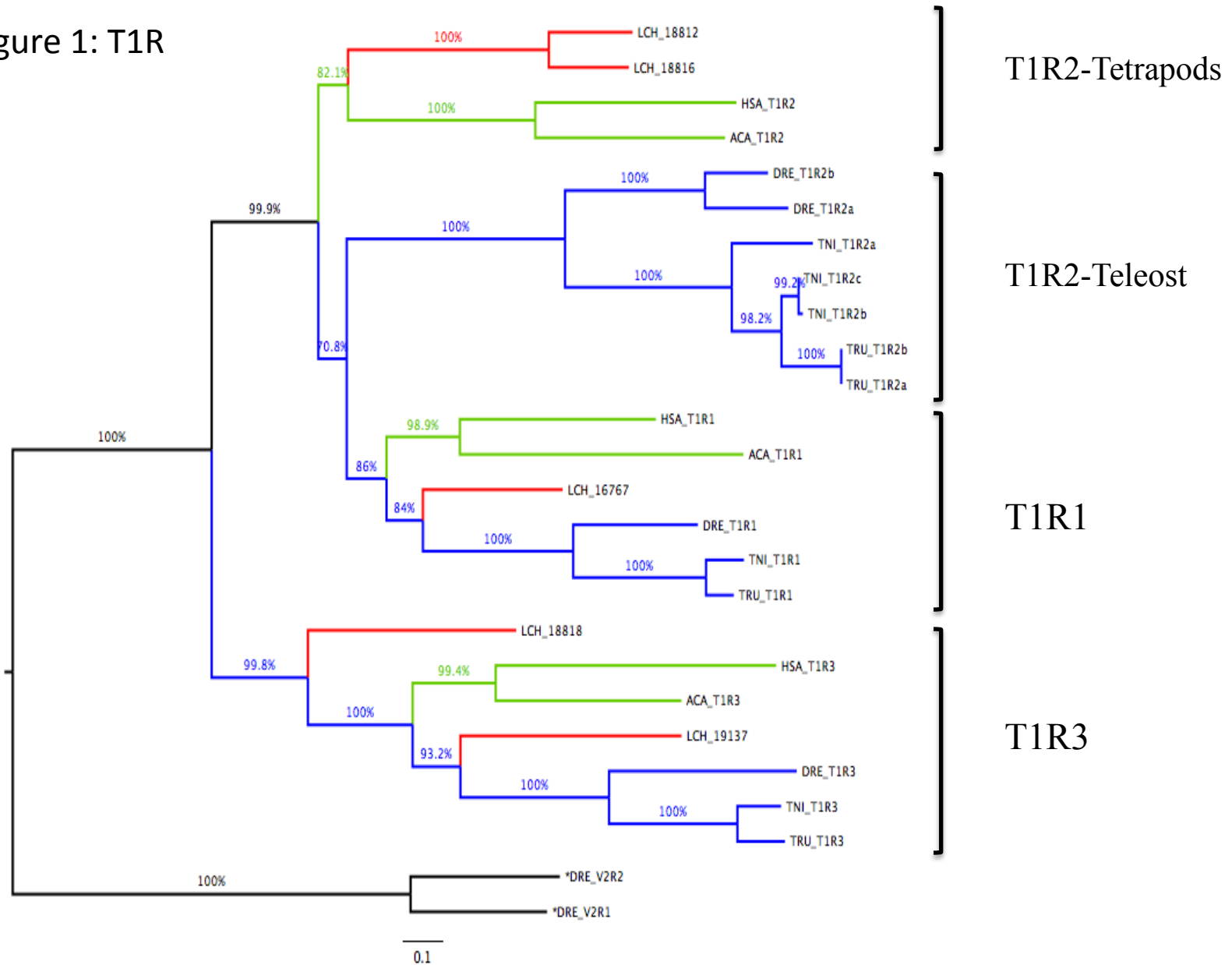


Figure 2: T2R

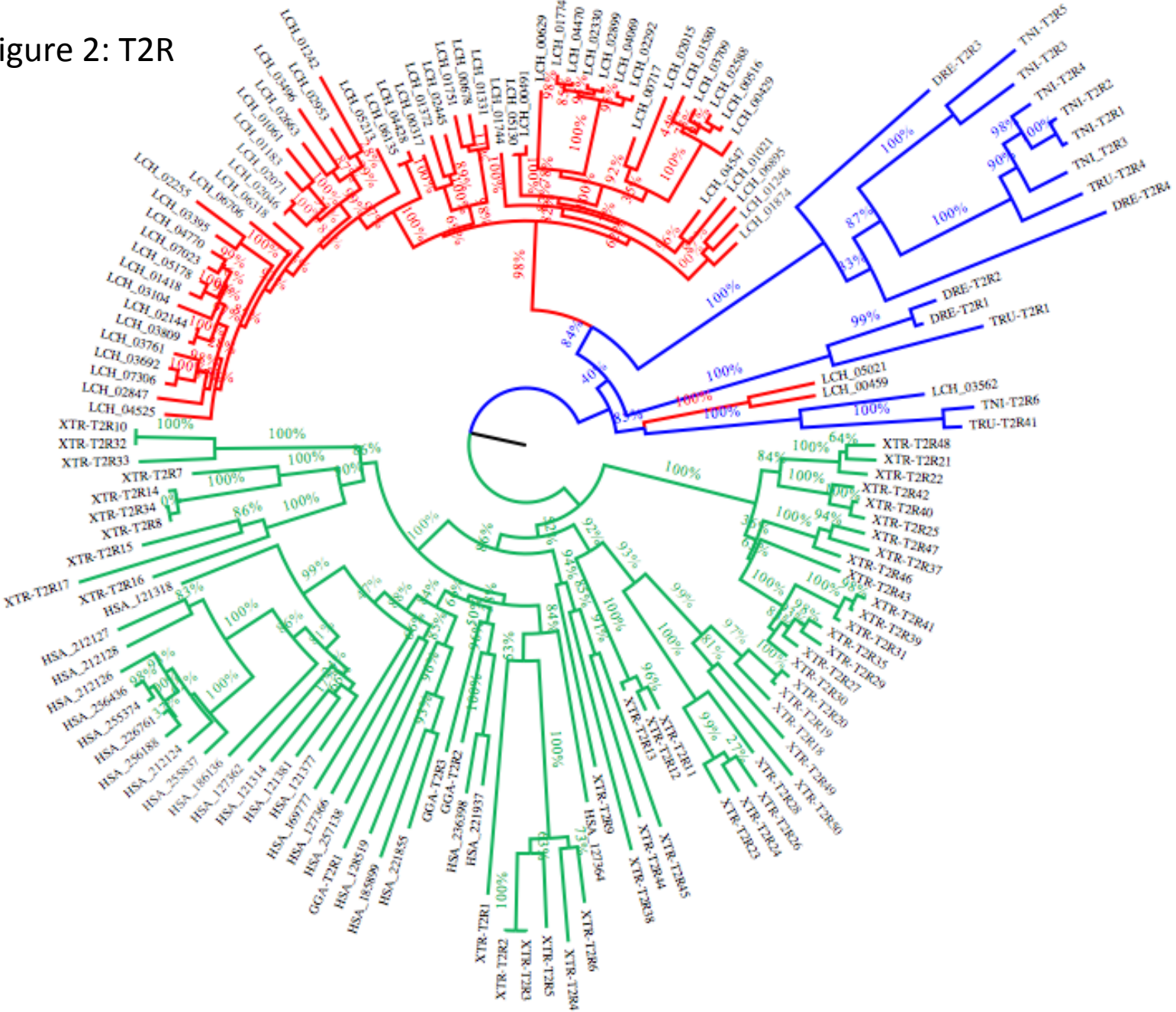


Figure 3: OR

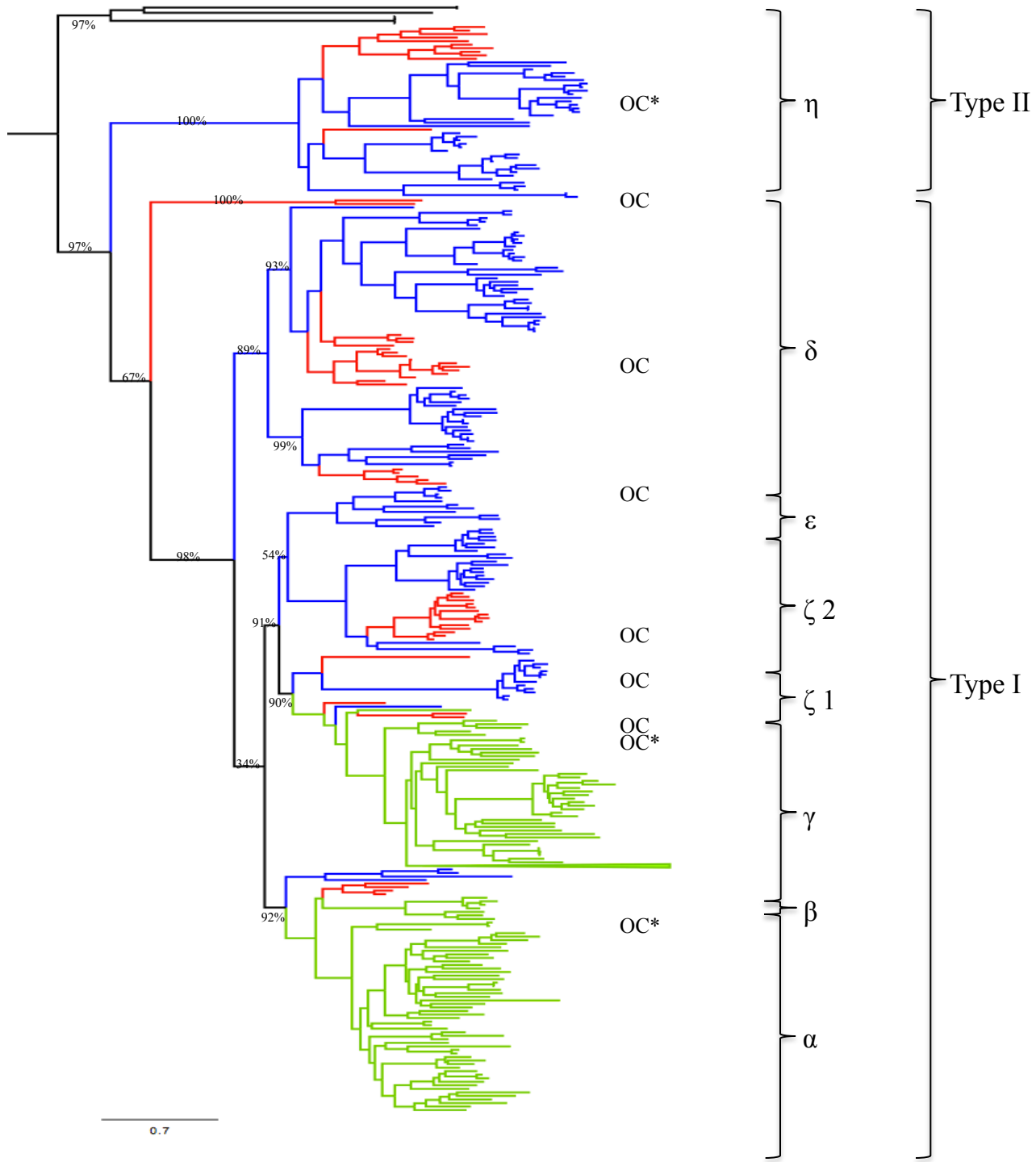


Figure 4: TAARs

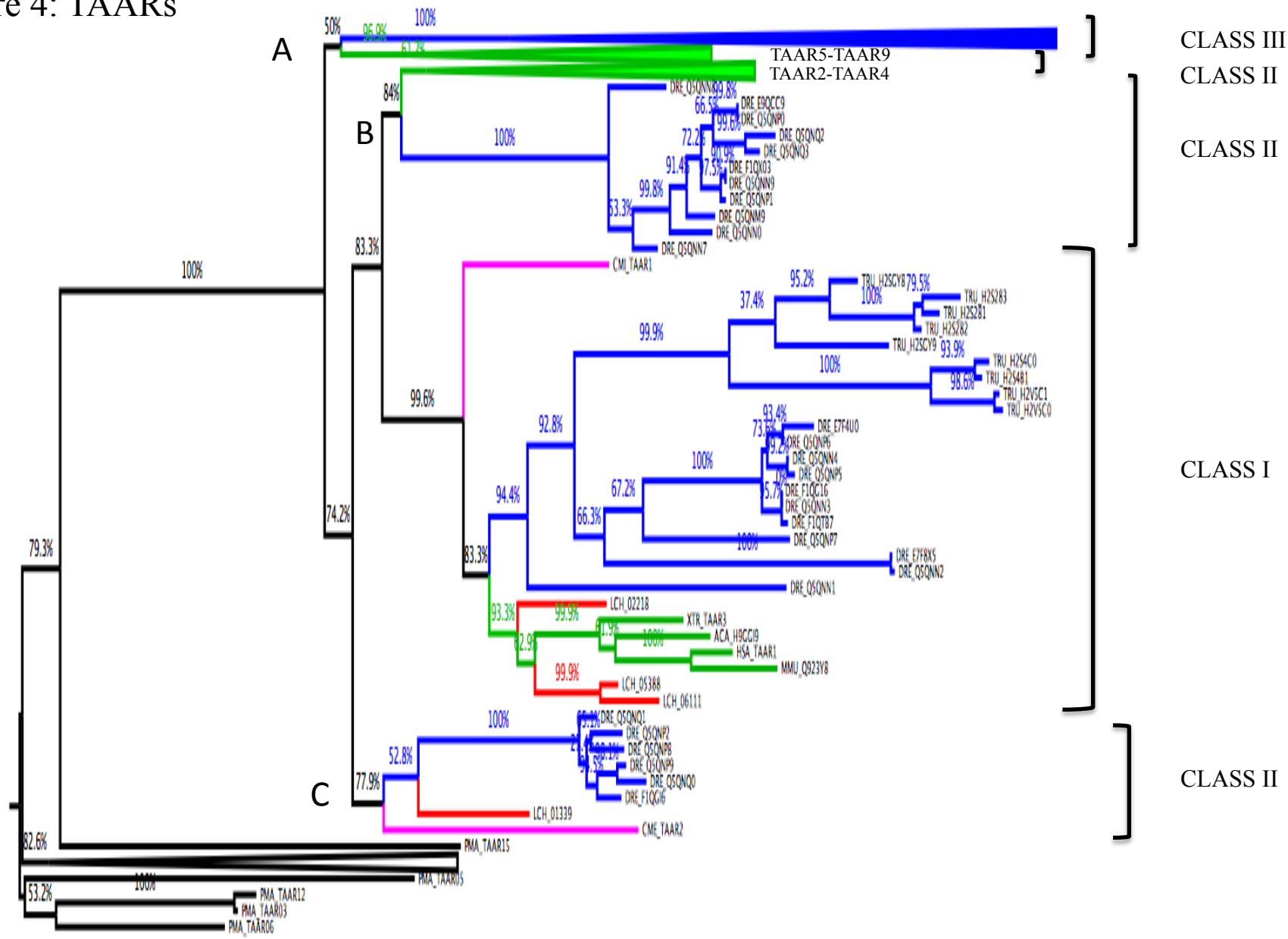


Figure 5: V1Rs

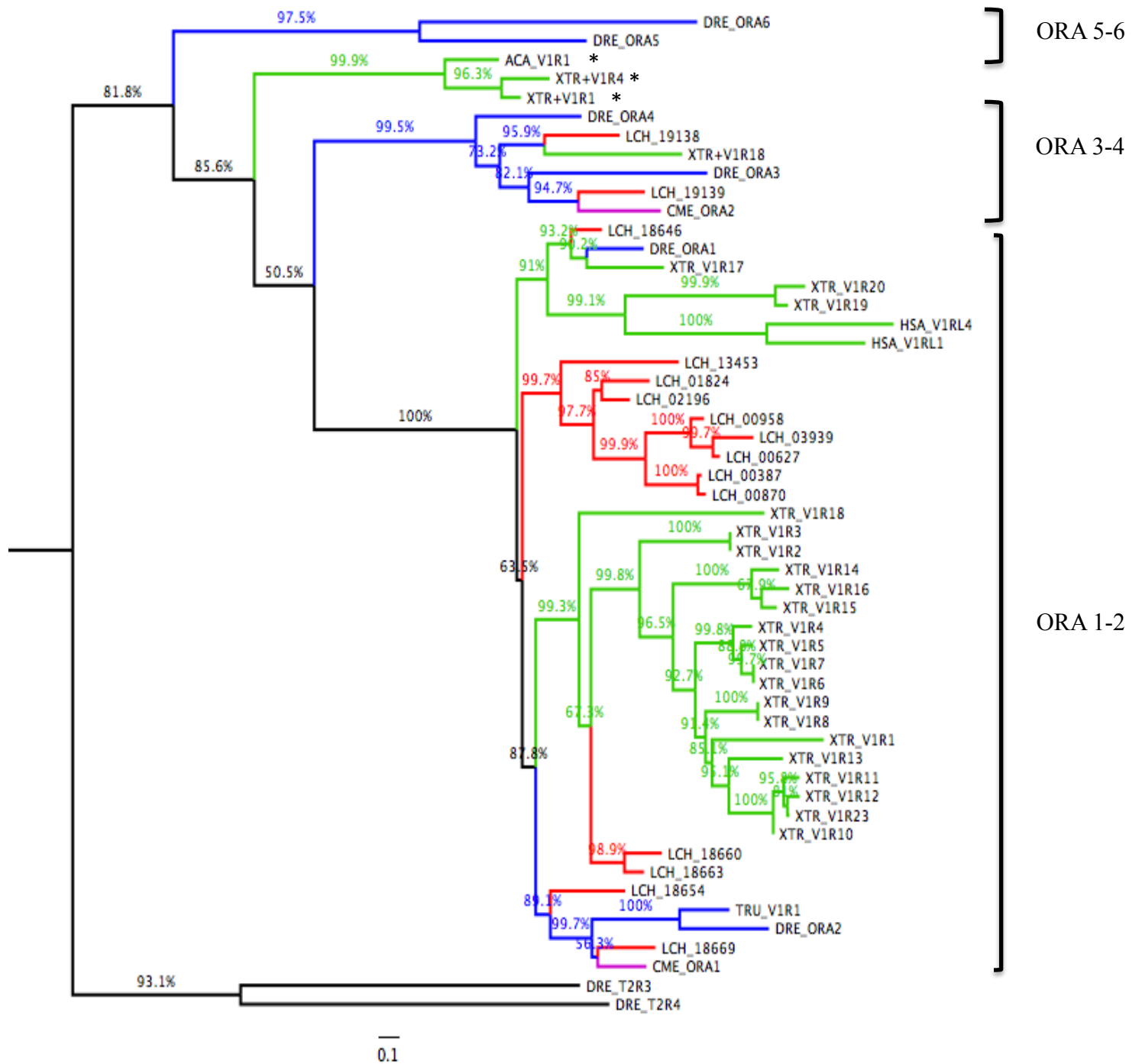


Figure 6: V2Rs

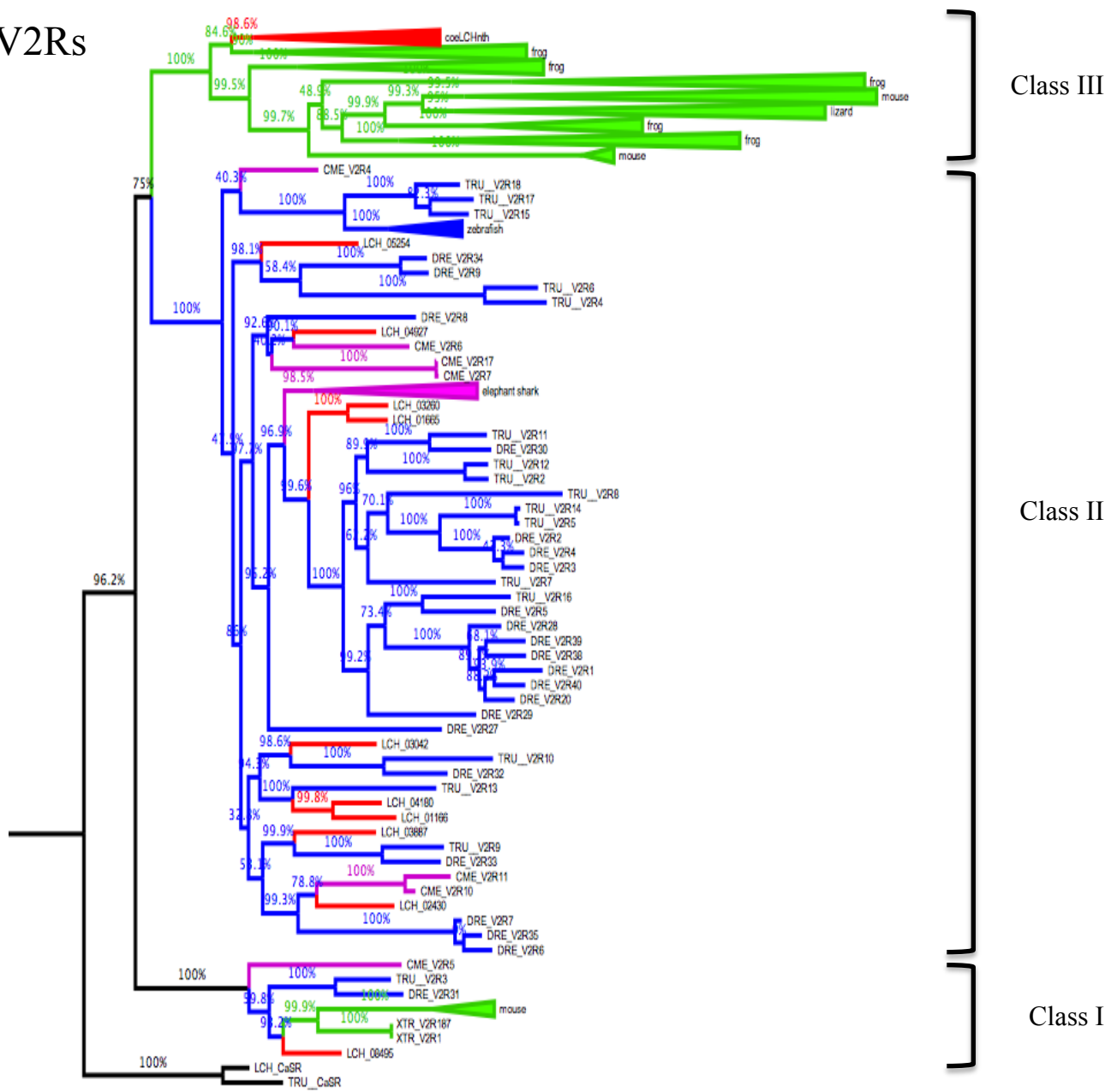


Figure S1: ORs

